



REVISIONS				
DATE	ORIGINATOR	CHECKER	ENG. APP.	NATL. AP.
20/01/2001	SCOTTRE2	HROBERT1	SCROUCH	
20/04/2001	SCOTTRE2	HROBERT1	SCROUCH	
18/06/2001	SCOTTRE2	HROBERT1	SCROUCH	

CIRCUIT SCHEMATIC DIAGRAM	
DATE 18 May 2001	<b>VISTEON</b>
PRODUCT ENGINEERING	
ELECTRONIC DESIGN S Cottrell	
R.F. DESIGN	
ECAD	
EMC	
MECHANICAL DESIGN K Gallichan	
BOARD NAME SKETCH - WIRING DIAGRAM	
B2xx Radio Front board Tape (B2)	
MECHANICAL DRG.	
PWB NO. 10131	SHT 1 OF 1